

Abstract

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Combining Cyclic Q delay and Cell Interleaver for Enhanced Performance DVB-T2 system

DVB-T2 (The second generation Digital Video Broadcasting) is mainly aimed to replace the current standard DVB-T. The main motivation of DVB-T2 is to provide broadcasters with more advanced and efficient alternative to DVB-T standards. The cyclic Q delay and the cell interleaver are both optional modules existing in the DVB-T2 system. The main purpose of the two modules is to increase the overall DVB-T2 system performance. In this paper we propose a new algorithm to combine the two modules. The new algorithm serves as a primary verification step before hardware implementation. The aim of the combined module is to decrease the system complexity, hardware usage and the overall system delay.