Solid State Relays & Its Applications

Presented By
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Course Objectives

- Know new techniques in relay industries.
- Understand the types of static relays and its components.
- Understand Digital relay fundamentals
- apply the different types of digital relays
Course contents

• Type of relay system
• General overview of static relays.
• Static Relay elements and design
• Computer applications to protective relaying.
• Digital relay fundamentals
• Mathematical Background of digital relay.
• Applications of Digital relay
• Advanced topics in relaying system
Course outcomes

• Based on NARS (National Accreditation reference Standard) the course should cover some intended learning outcomes

1- Knowledge and Understanding

2- skills

  – 1- Intellectual Skills
  – 2- Practical Skills
  – 3- General Skills
Knowledge and understanding

- Understand electromechanical relays versus static and digital relay.
- Know recent developments in the area of microprocessor relays and protection systems.
- Demonstrate the utilization and various applications of digital relays in integrated apparatus protection.
- Know how to integrate protection and control in substations.
- Distinguish between different mathematical tools used in digital relay.
Intellectual skills

• Determine the relay function and classify the relay types
• Determine the relay components
• Select the suitable mathematical tools to build digital relay algorithm
• Demonstrate the hardware and software of digital relay
• Report the main differences between digital and static relay
• Apply digital relay for different electric equipments
Practical (Professional) Skills

• Design a complete static relay
• Select the hardware that is suitable for use in digital relays effectively.
• Design a digital relay for different electric equipment.
• Differentiate between the suitable relay algorithm for a specified relay function
• Manipulate the measured signal numerically
• Performing laboratory experiments to get familiar with different types of protective relays
General skills

• Communicate effectively with colleagues and professional engineers to interchange knowledge and information in the power engineering field of practice.
• Prepare effective and informative technical reports and present results on electrical power engineering.
• Using information technology (IT) to follow the continuous advancement in electrical power innovations and technological applications.
• Practice digital relay operation and tuning
Lecture 1: Introduction

- Introduction to relaying system
- Multi-layer structure of power system
- Elements of protection system
- Type of protection devices
- Type of relaying system
Protective Relaying

- Power system is an essential and the main stem of modern industrial society, therefore increasing availability, safety and quality of power system is mandatory.
- The power system is always subject to almost un-measurable and unpredictable disturbances created by different abnormal reasons such as:
  - random load changes
  - Equipment and operator failure
  - Faults due to natural causes
Protective Relaying

– The function of protective relaying is to promptly remove from service any element from the power system that starts to operate in an abnormal manner.

– The purpose is
  • Limit further damage of equipment
  • Minimize danger to people
  • Reduce stress on other equipments
  • Maintain integrity and stability
• Protective relaying is a device that detects abnormal power system conditions and initiates a corrective action as fast as possible in order to return the power system to its normal state. The response must be automatic, quick and cause minimum amount of disruption of power system.

• Main requirements of protective system
  – Quick response
  – Correct diagnosis of trouble
  – Minimum disturbance to power system
  – Absolute discrimination
  – Sensitive and accurate
  – Reliable
  – Stable

Therefore expensive and sophisticated equipment is needed to accomplish these tasks.
Multi-layer structure of power system

- Three layered structure of power system
  - Apparatus such as (generator, Transformer, etc.)
  - Control Eq. which control voltage, frequency, power, optimal economic and security in interconnected network
  - Relay: more fast than control and it acts to open and close Circuit Breaker (CB), which leads to changing structure of power system. While control acts continuously adjust system variations.
Elements of a Protection system

- **Transducer**
  - Such as P.T and C.T.

- **Breaker**
  - CB to isolate the fault

- **Relay**
  - To detect fault and remove it

- **Supply**
  - To produce tripping power of breaker and operate relay
  - Should be uninterrupted such as batteries and UPS
Relay Construction

- Based on the elements of protection system, the relay construction can be divided into three subsystems:
  - Sensing
  - Comparison
  - Control action (trip)
Relay Function

• The relay function is depending on the abnormal situation that should be protected so, the relay may be
  – Over load
  – Over and under voltage
  – Over and under frequency
  – Reverse power
  – Short circuit
  – Etc.
  – The selection of relaying function depends mainly on the equipment that should be protected
Relay Types

- The relay types can be classified based on the techniques used to perform its function such as
  - Conventional (Electro-mechanical relay)
  - Solid state relay
  - Digital relay
Conventional relay

• It is an electro-mechanical device, it has the advantage that
  – Robust
  – Easy to maintenance
– While it has disadvantage
  • Large size
  • High power consumption
  • It depends on moving parts which need more maintenance
  • Each relay perform a single function
Example

• Discuss the basic structure of over current relay (OC)
Lecture 2: Solid state relay Fundamentals

• Basic fundamental of static relay
• Logic Signal conditioning
• Analog Signal Conditioning
• Comparator circuits
• Time delay circuit
• Example of static relay
Solid State Relay

- It has no moving parts most of its components are static device based on semi-conductor materials.
- The main parts

Power system
- sensing
- Circuit Breaker (CB)

Relay circuit
- protection
- Trip Circuit
- Transducer and signal conversion
- Time delay function
- Comparator function
- Signal conditioning (Filtration)
- Relay logic
Solid State Relay

1- Sensing: voltage and current sensors such as CT and PT
2- Protection: Ziner diode, current limiter, etc...
3- Transducer: such as hall transducer, F/V, AC-DC, etc...
4- Signal conditioning: Low pass filter,...
5- Comparer: voltage and current comparators, ... 
6- Time delay circuit: trip time calculation circuit
7- Trip circuit: Signal conditioning to operate CB
Solid State Relay

• Advantages:
  – Small size
  – Easy to mounted
  – Fast response
  – Low power consumption
  – No moving parts (less maintenance)
  – Can perform more sophisticated function

• Disadvantages
  • Less robust to vibration & Temp.
  • Different circuit for different function
  • Less flexible
Signal Conditioning

• It is the Adjustment of signal to be suitable to the next stage (controller, display, etc...) for example
  – Amplification
  – Attenuation
  – Filtration
  – Isolation
  – Linearization
  – Excitation
  – Conversion
  – Etc.
• Analog signal conditioning
• Binary signal Conditioning
• Digital Signal Conditioning
Logic Signal Conditioning

• Two state signal (high-low)
  – High: 5v, 12V, 24v (dc)
    – 24, 110, 220 v (ac)

• Buffer (Isolation)
  – Tri-state buffer
  – Using Transistor
Logic Signal Conditioning (cont.)

• Opt. isolation
  – Photo Diode
  – Photo transistor
  – Photo triac

• Relay

• 2. Inverter

• 3. Amplifier
Analog signal Conditioning

• It Depends mainly on OP-Amp

\[ V_{out} = A(V_2 - V_1) \]

- \( V_{out} \) = Output voltage.
- \( A \) = Open-loop gain.
- \( V_1 \) = Inverting input.
- \( V_2 \) = Noninverting input.
Analog signal Conditioning

- It Depends mainly on OP-Amp

- Unity Gain

*Buffer*

\[ V_{out} = V_{in} \]

The buffer can be used to keep the voltage level constant but increasing the current (take the current from the power supply circuit)
Analog Signal Conditioning (Cont.)

• Addition and subtraction
• Integrator
• Linearization
• Comparator
  – Normal comparator
  – Hysteresis

[Diagram of a comparator with input and output voltages]
Analog signal conditioning

- **Inverting Amplifier**

  \[ V_{out} = -V_{in} \left( \frac{R_f}{R_i} \right) \]

  The inverting amplifier can amplify the signal with certain gain but with inverting the polarity of the input signal.

- **Non-inverting Amplifier**

  \[ V_{out} = V_{in} \left( \left( \frac{R_f}{R_i} \right) + 1 \right) \]

  The non-inverting amplifier can amplify the signal with certain gain with the same polarity of the input signal.
Analog signal Conditioning

- Unity Gain
- Amplifier
Analog Signal Conditioning (Cont.)

• Addition and subtraction

\[
\begin{align*}
V_{OUTN} &= -\frac{R_F}{R_N} V_N \\
V_{OUT1} &= -\frac{R_F}{R_1} V_1 \\
V_{OUT2} &= -\frac{R_F}{R_2} V_2 \\
V_{OUT} &= -\left(\frac{R_F}{R_1} V_1 + \frac{R_F}{R_2} V_2 + \frac{R_F}{R_N} V_N\right)
\end{align*}
\]
Differential amp

\[ V_+ = V_1 \frac{R_2}{R_1 + R_2} \]

\[ V_{OUT1} = V_+(G_+) = V_1 \frac{R_2}{R_1 + R_2} \left( \frac{R_3 + R_4}{R_3} \right) \]

\[ V_{OUT2} = V_2 \left( -\frac{R_4}{R_3} \right) \]

\[ V_{OUT} = V_1 \frac{R_2}{R_1 + R_2} \left( \frac{R_3 + R_4}{R_3} \right) - V_2 \frac{R_4}{R_3} \]

When \( R_2 = R_4 \) and \( R_1 = R_3 \), Equation 3–13 reduces to Equation 3–14.

\[ V_{OUT} = (V_1 - V_2) \frac{R_4}{R_3} \]
Analog signal conditioning

Analog filters

**Low-Pass Filter**

\[
\frac{V_{OUT}}{V_{IN}} = 1 + \frac{X_C \parallel R_F}{R_G}
\]

\[
\frac{V_{OUT}}{V_{IN}} = \frac{X_C}{R_G}
\]

**High-Pass Filter**

\[
\frac{V_{OUT}}{V_{IN}} = 1 + \frac{R_F}{X_C \parallel R_G}
\]

\[
\frac{V_{OUT}}{V_{IN}} = \frac{1}{X_C}
\]
Analog signal conditioning

- Signal Integration

\[ V_o = -\frac{1}{R_G C_F} \int_0^t V_i(t) dt \]
Analog signal conditioning

• Signal Conversion
  – Current to voltage
  – Voltage to current

• Assignment
  – Write a report about voltage-to-current and current-to-voltage methods
Comparator Circuit

- Amplitude (Direct or Integration)
- Phase (Direct or Integration)
- Hybrid (Direct or Integration)
Time delay Circuit

• The simple circuit to generate time delay is RC circuit
\[ V_c = V_i \left(1 - e^{-\frac{t}{RC}}\right) \]
\[ t = RC \log\left(\frac{V_i}{V_i - V}\right) \]

• It can be built using digital timer
Comp

$V_i = \frac{R_2}{R_1}(V_s - V_p)$

$R_1 = R_2
R_2 = R_4$

time delay of over voltage

Logic Comp
Example: over current relay

\[ V_o = -IR \]

or hall sensor

protection relay

filter
Design a phase sequence relay.
Digital Relay Fundamentals

• Introduction
• Digital relay components
• Mathematical background
  – How to implement discrete differentiation and integration
  – How to estimate a value between two samples or predict a value in the future
Introduction

• Background
• Advantages of digital relay
• Overview of Digital relay structure
Fig. 1—Basic Configuration and External View of Compact-Type Digital Protection and Control Equipment for Power Systems.
Difference equation

1. Backward differences
   1. For continuous function $f(t)$
   2. $f_k = f(t_0 + kh)$
   3. $\nabla f_k = f_k - f_{k-1}$
   4. $\nabla^n f_k = \nabla^{n-1} f_k - \nabla^{n-1} f_{k-1}$

   Where $t_0$ is the initial value, $h$ the sampling interval, $k$ the sampling number

• Forward differences
  1. $\Delta f_k = f_{k+1} - f_k$
  2. $\Delta^n f_k = \Delta^{n-1} f_{k+1} - \Delta^{n-1} f_k$

1. Shift operator
   1. $f_{k+1} = f(t_k + h) = Ef_k$
   2. $f_{k+r} = f(t_k + rh) = E^r f_k$
   3. $E = 1 + \Delta$

$\nabla = 1 - E^{-1}$
Forward interpolation

\[
\begin{align*}
    f_{i+1} &= f(x_i + h) \\
    &= E_h f_i \\
    s_{k+p} &= (1 + \Delta y_s) f_i \\
    &= [1 + k_{1f} \Delta + k_{2f} \Delta^2 + k_{3f} \Delta^3 + \ldots + k_{pf} \Delta^p] f_i
\end{align*}
\]

where any value \( k_{mj} \) is often known as the \( m \)th binomial coefficient, given by

\[
k_{mj} = \frac{p(p-1)(p-2)\ldots(p-m+1)}{m!}
\]

\[
\begin{align*}
    s_{k+p} &= s_k + p \Delta s_k + \frac{p(p-1)}{2!} \Delta^2 s_k + \frac{p(p-1)(p-2)}{3!} \Delta^3 s_k + \ldots + \frac{p(p-1)(p-2)\ldots(p-n+1)}{n!} \Delta^n s_k
\end{align*}
\]

Backward interpolation

\[
\begin{align*}
    f_{N+1} &= E^p f_N \\
    &= f_N (1 - \nabla)^p f_N
\end{align*}
\]

\[
\begin{align*}
    s_{N+p} &= [1 - k_{1b} \nabla + k_{2b} \nabla^2 - k_{3b} \nabla^3 + \ldots + k_{pb} \nabla^p] f_N
\end{align*}
\]

The binomial coefficients \( k_{1b}, k_{2b}, \ldots \) take the form

\[
k_{mb} = \frac{p(p+1)(p+2)\ldots(p+m-1)}{m!}
\]

\[
\begin{align*}
    s_{N+p} &= f(x_N + sh) = f_N + s \nabla f_N + \frac{s(s+1)}{2!} \nabla^2 f_N + \frac{s(s+1)(s+2)}{3!} \nabla^3 f_N + \ldots + \frac{s(s+1)(s+2)\ldots(s+n-1)}{n!} \nabla^n f_N
\end{align*}
\]
Estimation and data Prediction

\[ f_{k+r} = (t_k + rh) = Ef_k, \quad 0 < r < 1 \]

using backward differences

\[ f_{k+r} = f_k + r\nabla f_k + \frac{r(r+1)}{2!} \nabla^2 f_k + \frac{r(r+1)(r+2)}{3!} \nabla^3 f_k + \cdots + \frac{r(r+1)(r+2)\ldots(r+n-1)}{n!} \nabla^n f_k \]

Using forward differences

\[ f_{k+r} = f_k + r\Delta f_k + \frac{r(r-1)}{2!} \Delta^2 f_k + \frac{r(r-1)(r-2)}{3!} \Delta^3 f_k + \cdots + \frac{r(r-1)(r-2)\ldots(r-n)}{n!} \Delta^n f_k \]

By replacing the differences operator by the equivalent form the new equation is called
• Example: find the recursive equation of the backward prediction function by approximating the result to the second term

\[
\begin{align*}
f_{k+r} &= f_k + r\Delta f_k + \frac{r(r-1)}{2!} \Delta^2 f_k, \\
f_{k+r} &= f_k + r(f_k - f_{k-1}) + \frac{r(r-1)}{2!} (\Delta f_k - \Delta f_{k-1}), \\
f_{k+r} &= f_k + r(f_k - f_{k-1}) + \frac{r(r-1)}{2!} (f_k - 2f_{k-1} - f_{k-2}), \\
f_{k+r} &= af_k + bf_{k-1} + cf_{k-2}
\end{align*}
\]

\[
\begin{align*}
a &= 1 + r + \frac{r(r-1)}{2!}, \\
b &= 2 \frac{r(r-1)}{2!} - r, \\
c &= -\frac{r(r-1)}{2!}
\end{align*}
\]
Numerical differentiation

\[ f'_k = \lim_{r \to 0} \frac{\partial}{\partial r} f_{k+r} = \lim_{r \to 0} \frac{\partial}{\partial r} f(t_k + rh) \]
\[ f''_k = \lim_{r \to 0} \frac{\partial^2}{\partial r^2} f_{k+r} = \lim_{r \to 0} \frac{\partial^2}{\partial r^2} f(t_k + rh) \]

**Backward**
\[ f'_k = f'(t_k) = \frac{1}{h} \left( \nabla + \frac{1}{2} \nabla^2 + \frac{1}{3} \nabla^3 + ... \right) f_k \]
\[ f''_k = f''(t_k) = \frac{1}{h^2} \left( \nabla^2 + \nabla^3 + \frac{11}{12} \nabla^4 + \frac{5}{6} \nabla^5 + ... \right) f_k \]

**Forward**
\[ f'_k = f'(t_k) = \frac{1}{h} \left( \Delta - \frac{1}{2} \Delta^2 + \frac{1}{3} \Delta^3 - ... \right) f_k \]
\[ f''_k = f''(t_k) = \frac{1}{h^2} \left( \Delta^2 - \Delta^3 + \frac{11}{12} \Delta^4 - ... \right) f_k \]
Numerical differentiation

- Approximate differentiation
  - First term
    
    \[ f_k' = f'(t_k) \approx \frac{1}{h} (\nabla)f_k \]
    
    \[ f_k' = f'(t_k) \approx \frac{1}{h} (f_k - f_{k-1}) \]
  
  - Second term
    
    \[ f_k' = f'(t_k) \approx \frac{1}{h} \left( \nabla + \frac{1}{2} \nabla^2 \right) f_k \]
    
    \[ f_k' = f'(t_k) \approx \frac{1}{h} \left( f_k - f_{k-1} + \frac{1}{2} (f_k - 2 f_{k-1} + f_{k-2}) \right) \]
    
    \[ f_k' \approx \frac{1}{h} \left( 1.5 f_k - 2 f_{k-1} + \frac{1}{2} f_{k-2} \right) \]
Numerical Integration

• Rectangle approximation
Sinusoidal-wave based algorithm

Chapter 4:
Pure sine wave algorithm
Build a program to deduce the following parameters
1- voltage magnitude
2- current magnitude
3- phase shift
4- circuit impedance (Z,R,x)
For a given data file (data1) where sampling time 1ms and the signal frequency 50hz each raw of the data1 file is [sample#, v(k), i(k)]